

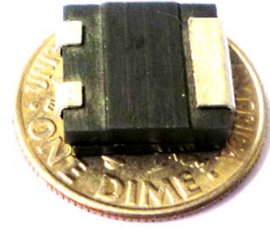


SLA4715 Series



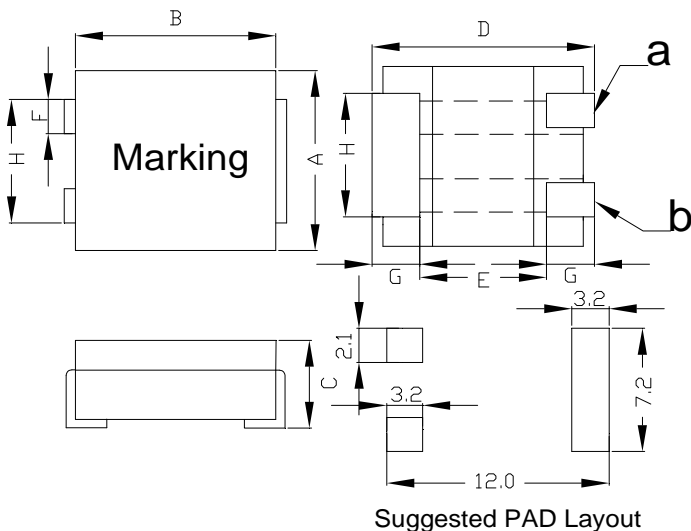
1. Features:

- Ferrite based SMD Inductor with lower core loss.
- Inductance Range:360nH to 560nH. Custom values are welcomed.
- High current output chokes, upto 36 Amp with approx. 20% roll off.
- Low Profile 4.0mm Max. height .
- Foot Print 12.0 x 10.4 mm Max.
- Ideal for Buck Converter, VRM & High Density Board Design.
- Operating frequency up to 1 MHz application.
- Operating Temperature Range -55°C to + 130°C , RoHs compliance ;



2. Electrical Characteristic of SLA4715 Series:

Part Number	Inductance (nH) ± 15%	DCR (mΩ) ± 7%	Isat ¹ (A) @25°C	L@Isat ¹ (nH) Typical	Isat ² (A) @100°C	Isat ³ (A) @125°C	Irms (A) @25°C	Height (mm) Max.
SLA4715D-R36LHF	360	1.00	36.0	306.0	28.0	26.0	27.0	4.00
SLA4715E-R36LHF	360	0.88	36.0	306.0	28.0	26.0	29.0	4.05
SLA4715D-R56LHF	560	1.00	22.5	476.0	17.0	15.0	27.0	4.00
SLA4715E-R56LHF	560	0.88	22.5	476.0	17.0	15.0	29.0	4.05



3. Mechanical Dimension(Unit:mm):

A	B	C	D	E	F	G	H
(Max.)	(Max.)	(Max.)	(Max.)	(Nom.)	(Nom.)	(Nom.)	(Nom.)
10.40	10.80	see below	12.00	6.50	1.50	2.54	6.60

Note:

- 1>.Open Circuit Inductance (OCL) test condition:100KHz,1.0Vrms ,0Adc.
- 2>.Full Load Inductance (FLL) Test condition:100KHz,1.0Vrms ,Isat.
- 3>.Isat¹,Isat² & Isat³: DC current that will cause inductance to drop approximately by 20%.
- 4>. Irms: DC current for an approximate temperature rise of 40°C without core loss,.Derating is necessary for AC currents. PCB pad layout, trace thickness and width, air-flow and proximity of other heat generating components will affect the temperature rise. It is recommended the part temperature not exceed 130°C under worst case operating conditions verified in the end application.
- 5>.The nominal DCR is measured from point "a" to point"b",as shown above on the mechanical drawing.



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Inductance vs. Current

